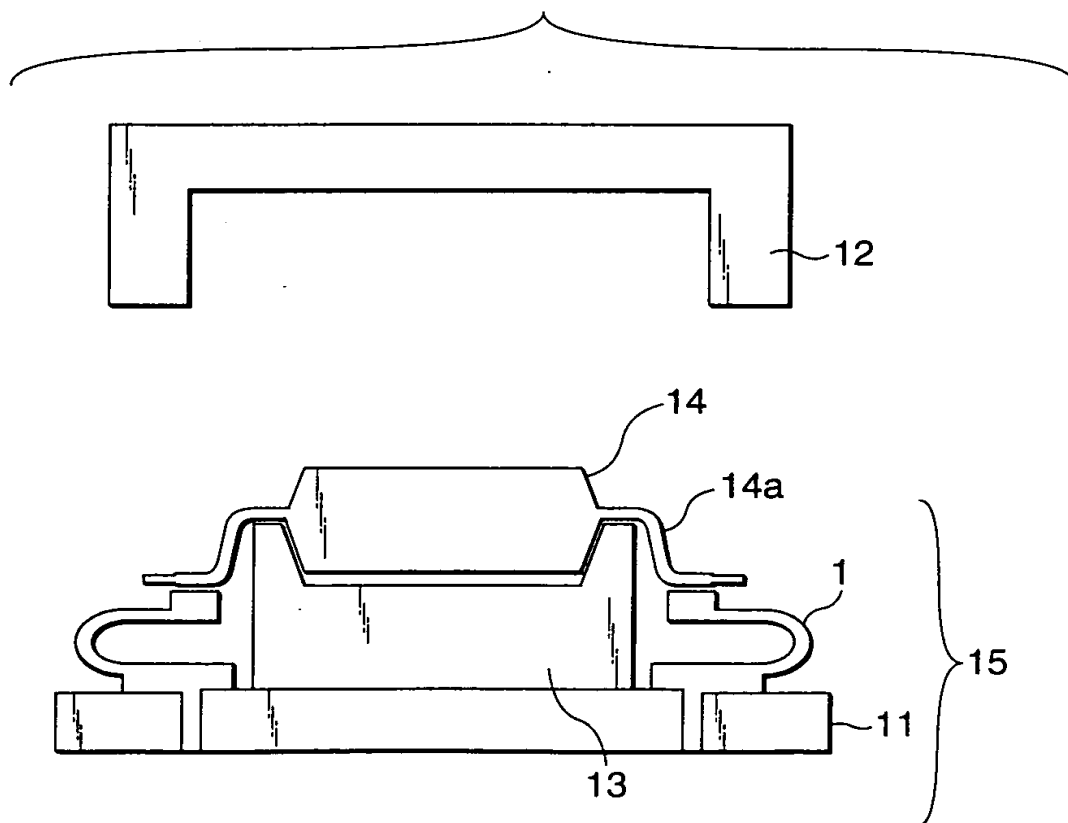


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FIG.18

	NUMBER OF LOADED ICs $n$	NUMBER OF DEFECTIVE ICs $m$	FRACTION DEFECTIVE $m/n$
CONTACT TERMINAL A	3016	12	0.4%
CONTACT TERMINAL B	3000	29	1.0%
CONTACT TERMINAL C	2900	61	2.1%

Prior Art  
FIG.19





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Prior Art

FIG.20

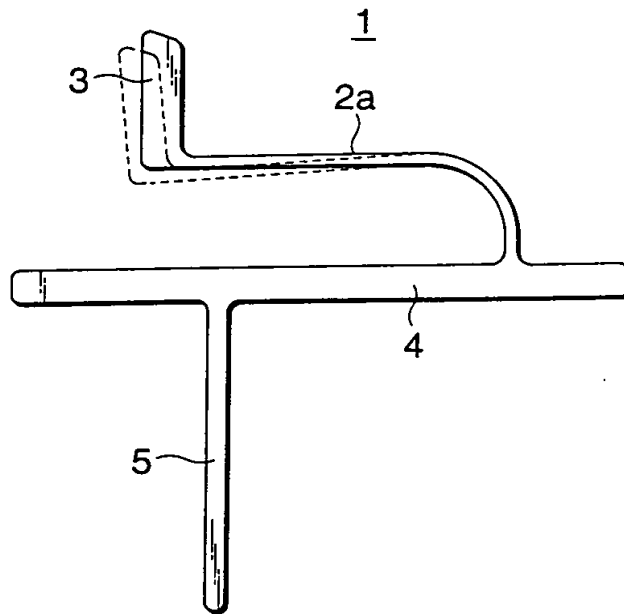
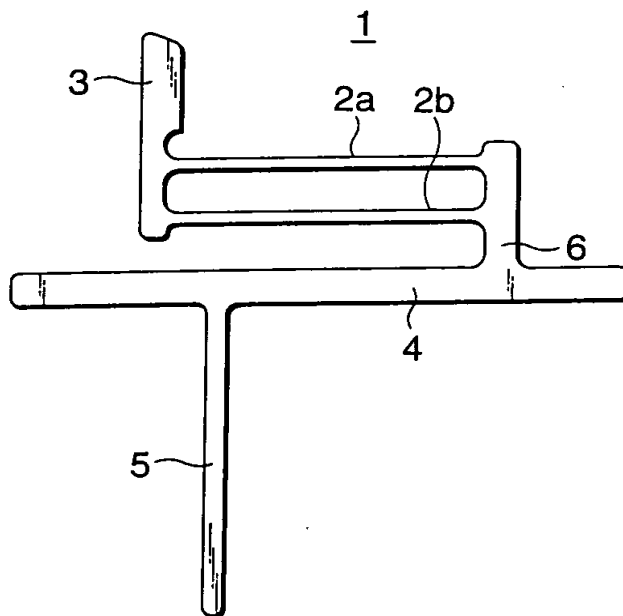


FIG.21

Prior Art.





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FIG.22 Prior Art

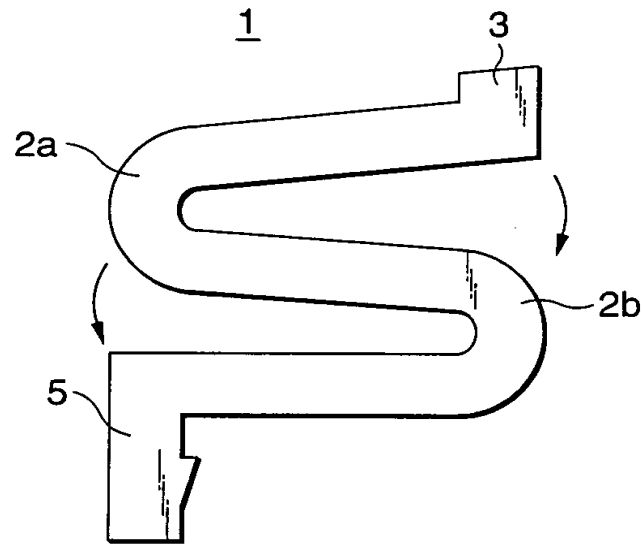


FIG.23 Prior Art

